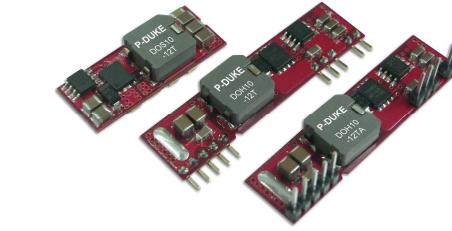


DOS10-12T DOH10-12T

DC-DC CONVERTER

UP TO 10 Amps



FEATURES

- NO MINIMUM LOAD REQUIRED
- SMALL SIZE AND LOW PROFILE :
SMD TYPE:1.30 X 0.53 X 0.30 INCH , SIP TYPE:2.00 X 0.50 X 0.28 INCH
- SMD PACKAGE QUALIFIED FOR LEADFREE REFLOW SOLDER PROCESS ACCORDING IPC J-STD-020D
- UL60950-1, EN60950-1, & IEC60950-1 SAFETY APPROVALS
- CE MARKED
- COMPLIANT TO RoHS II & REACH

APPLICATIONS

- WIRELESS NETWORK
- TELECOM/DATACOM
- INDUSTRY CONTROL SYSTEM
- DISTRIBUTED POWER ARCHITECTURES
- SEMICONDUCTOR EQUIPMENT
- MICROPROCESSOR POWER APPLICATIONS

REMOTE CONTROL UVP OCP SCP

TECHNICAL SPECIFICATION

All specifications are typical at nominal input, full load and 25°C otherwise noted

Model Number	Input Range VDC	Output Voltage VDC	Output Current @Full Load A	Input Current Vin(nom) @ No Load	Efficiency Vin(nom),3.3VDC @Full Load %	Maximum Capacitor Load (1) ESR \geq 1mΩ / ESR \geq 10mΩ μF
				0.75VDC / 5.0VDC mA		
DOS10-12T	Vout(set) \leq 3.63					
DOS10-12T-P	Vin = 8.3 ~ 14					
DOH10-12T						
DOH10-12T-P						
DOH10-12TA	Vout(set) $>$ 3.63					
DOH10-12TA-P	Vin = 8.3 ~ 13.2					

PART NUMBER STRUCTURE

DOS10	- 12	T	- P
Series Name	Input Voltage (VDC)	Package	Remote Control Option
DOS10: SMD TYPE DOH10: SIP TYPE	12: 8.3~14	SMD TYPE SIP TYPE T:No Assembly T:Vertical Mounting SIP TA:Horizontal Mounting SIP	<input type="checkbox"/> Negative Logic <input checked="" type="checkbox"/> Positive Logic

INPUT SPECIFICATIONS

Parameter	Conditions	Min.	Typ.	Max.	Unit
Operating input voltage range	Vout(set) \leq 3.63VDC Vout(set) $>$ 3.63VDC	8.3	12	14	VDC
Maximum input current	Vin=8.3 to 14VDC, Io=Io(max.)	8.3	12	13.2	A
Input reflected ripple current	5~20MHz, 1μH source impedance		20		mAp-p
Start-up voltage			7.9		VDC
Shutdown voltage			7.8		VDC
Input filter (2)					Capacitor type

OUTPUT SPECIFICATIONS

Parameter	Conditions	Min.	Typ.	Max.	Unit
Voltage accuracy	% of Vout(set)	-2.0		+2.0	%
Line regulation	Vin=Vin(min.) to Vin(max.) at Full Load	% of Vout(set)	-0.3	+0.3	%
Load regulation	No Load to Full Load	% of Vout(set)	-0.4	+0.4	%
Voltage adjustability ⁽³⁾		0.7525	5	VDC	
Ripple and noise	Measured by 20MHz bandwidth, With a 1μF MLCC & a 10μF T/C		30 75	mVrms mVp-p	
Temperature coefficient		-0.4		+0.4	%/°C
Dynamic load response	With a 1μF MLCC & a 10μF T/C △Io/△t=2.5A/μs,Vin(nom) 50% load step change	Peak deviation Setting time(Vout<10%peak deviation)	200 25	mV μs	
Dynamic load response	With 2pcs of 150μF polymer capacitors △Io/△t=2.5A/μs,Vin(nom) 50% load step change	Peak deviation Setting time(Vout<10%peak deviation)	100 25	mV μs	
Over load protection	% of Iout rated		200	%	
Short circuit protection			Continuous, automatics recovery		
Output voltage overshoot-startup	Vin= Vin(min.) to Vin(max.) at Full Load	% of Vout(set)	1.0	%	

GENERAL SPECIFICATIONS

Parameter	Conditions	Min.	Typ.	Max.	Unit
Isolation voltage					None
Switching frequency		270	300	330	kHz
Safety approvals					UL60950-1 EN60950-1 IEC60950-1
Weight					6.0g (0.21oz)
MTBF	MIL-HDBK-217F, Full load				3.355 × 10 ⁶ hrs

ENVIRONMENTAL SPECIFICATIONS

Parameter	Conditions	Min.	Typ.	Max.	Unit
Operating ambient temperature	With derating	-40	+85		°C
Over temperature protection			125		°C
Storage temperature range		-55		+125	°C
Thermal shock					MIL-STD-810F
Vibration					MIL-STD-810F
Relative humidity(non-condensing)					5% to 95% RH
Lead-free reflow solder process					IPC J-STD-020D
Moisture sensitivity level(MSL)					IPC J-STD-033B Level 2a

FEATURE SPECIFICATIONS

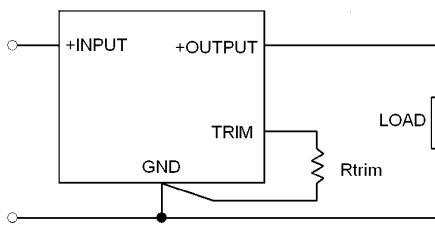
Parameter	Conditions	Min.	Typ.	Max.	Unit
Remote ON/OFF ⁽⁴⁾	Referred to -Vin pin Negative logic (Standard) Positive logic (Option) Input current of Ctrl pin Remote off input current	DC-DC ON DC-DC OFF DC-DC ON DC-DC OFF 0 ~ 0.3VDC Open or (Vin-4) ~ Vin(max.) 0 ~ 0.3VDC Open or 0 ~ 0.3VDC 2.5VDC ~ Vin(max.) 0.01 2.0		1.0	mA mA
Remote sense range				0.5	VDC
Rise time	Time for Vout to rise from 10% to 90%of Vout(set)			6	ms
Turn-on delay time	Case 1 ⁽⁵⁾ , Case 2 ⁽⁶⁾		3		ms

Note:

1. Test by minimum input and constant resistive load.
2. It's necessary to equip the external input capacitors at the input of the module. The capacitors should connect as close as possible to the input terminals that ensuring module stability. The external C_{in} is 4pcs of $47\mu F$ ceramic capacitors at least.
3. Output voltage programmable from 0.7525V to 5V by connecting a single resistor (shown as Trim Table) between the Trim and GND pins of the module. To calculate the value of the resistor R_{trim} for a particular output voltage V_{out} , use the following equation:

$$R_{trim} = \left[\frac{10500}{V_{out} - 0.7525} - 1000 \right] \Omega$$

Trim Figure



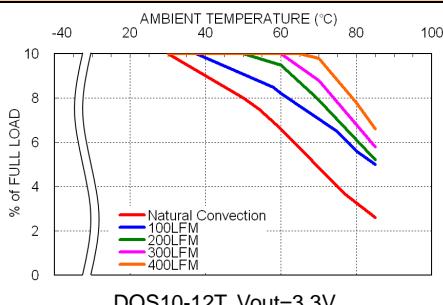
Trim Table

Vout(set) (VDC)	Rtrim (kΩ)
0.7525	Open
1.2	22.46
1.5	13.05
1.8	9.024
2.5	5.009
3.3	3.122
5	1.472

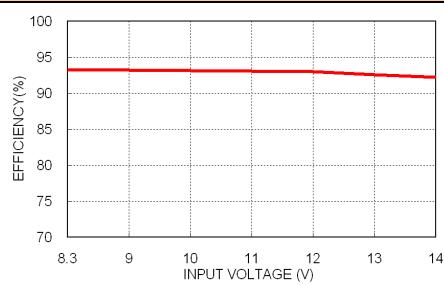
4. Positive logic:ON/OFF is open collector/drain logic input
Negative logic:ON/OFF pin is open collector/drain logic input with external pull –up resistor
5. Case 1: ON/OFF input is set to logic low (module on) and then input power is applied (delay from instant at which $V_{in}=V_{in(\min.)}$ until $V_{out}=10\%$ of $V_{out(\text{set})}$)
6. Case 2: Input power is applied for at least one second and then the ON/OFF input is set to logic low (delay from instant at which $V_{on/off}=0.3\text{VDC}$ until $V_{out}=10\%$ of $V_{out(\text{set})}$)

CAUTION: This power module is not internally fused. An input line fuse must always be used.

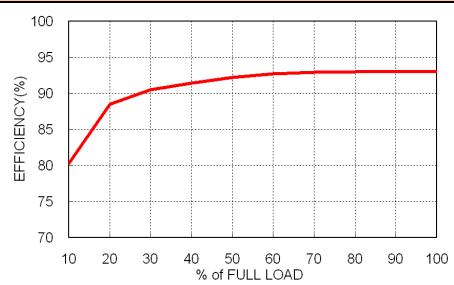
CHARACTERISTIC CURVE



DOS10-12T, Vout=3.3V
Derating Curve



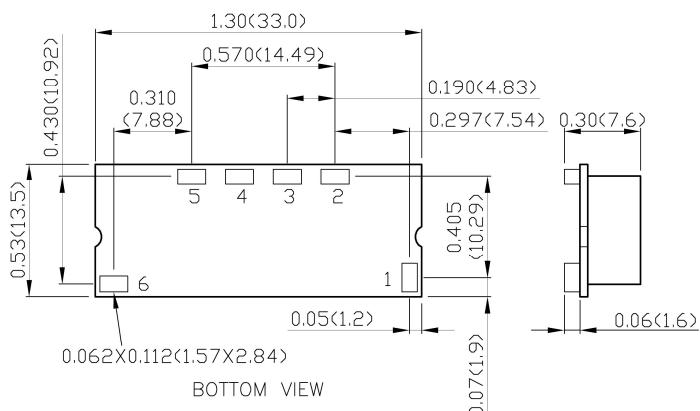
DOS10-12T, Vout=3.3V
Efficiency vs. Input Voltage



DOS10-12T, Vout=3.3V
Efficiency vs. Output Load

MECHANICAL DRAWING

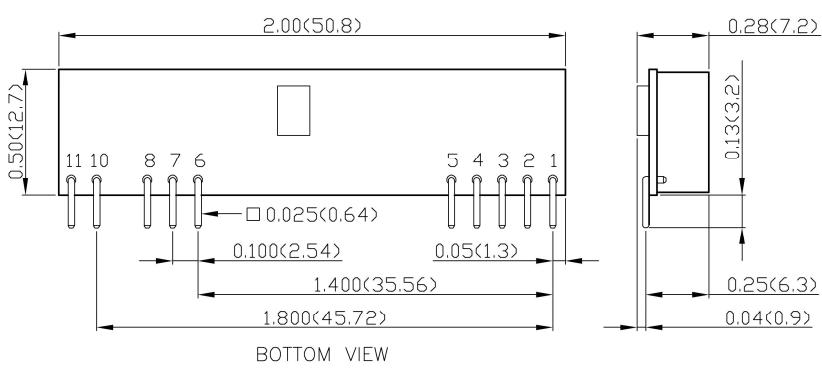
DOS10-12T



PIN CONNECTION

PIN	DEFINE
1	Ctrl
2	+Sense
3	Trim
4	+Vout
5	GND
6	+Vin

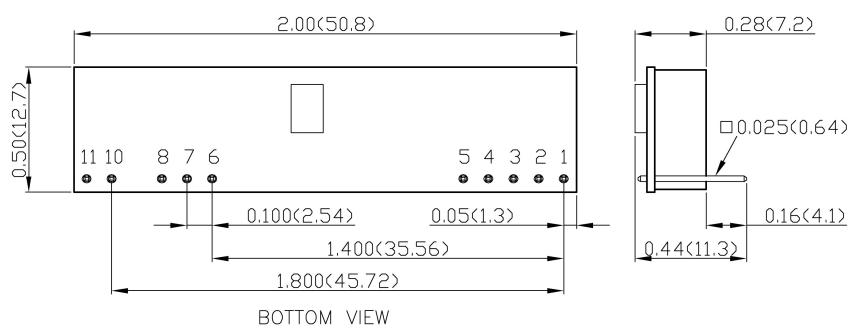
DOH10-12T



PIN CONNECTION

PIN	DEFINE
1	+Vout
2	+Vout
3	+Sense
4	+Vout
5	GND
6	GND
7	+Vin
8	+Vin
10	Trim
11	Ctrl

DOH10-12TA



PIN CONNECTION

PIN	DEFINE
1	+Vout
2	+Vout
3	+Sense
4	+Vout
5	GND
6	GND
7	+Vin
8	+Vin
10	Trim
11	Ctrl

1. All dimensions in inch (mm)
2. Tolerance : $x.x\pm 0.02$ ($x.x\pm 0.5$)
 $x.x\pm 0.01$ ($x.x\pm 0.25$)
3. Pin pitch tolerance ± 0.01 (0.25)
4. Pin dimension tolerance ± 0.004 (0.1)